



# TechTalks@SJBIT

SJB Institute of Technology, Bangalore

## Distinguished Lecture Series – Autumn 2010

*Date:*

**29<sup>th</sup> September 2010**

**Wednesday**

*Time:*

**2:30 PM**

*Venue:*

**Seminar Hall  
CSE Block, SJBIT**

*Address:*

**SJB Institute of Technology  
BGS Health & Education City  
Uttarahalli Main Road  
Kengeri, Bangalore 560060**

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### On Steganography and Watermarking

Dr. N. Rama Murthy

Head, Signal Processing Group & Additional Director  
Centre for Artificial Intelligence and Robotics, Bangalore

Steganography and digital watermarking are two areas of active research both of which fall in the domain of Information Hiding. While Steganography studies ways of making communication invisible by hiding secrets in innocuous messages, Watermarking aims at providing solutions for Digital Rights Management (DRM). The potential applications of these technologies are far too many ranging from civilian applications such as in health care industry to law enforcement to unobtrusive military communications. Information hiding brings together researchers from very different backgrounds: Electrical Engineering, Signal and Image Processing, Computer Science, and Cryptography to name a few. Dr. Murthy will survey the technological progress in the area of Information hiding in audio and images.

Dr. N. Rama Murthy is the Head of the Signal Processing Group and Additional Director in charge of Academic Collaborations at DRDO's Centre for Artificial Intelligence and Robotics. He leads research in real-time DSP algorithms, Cryptography and VLSI architectures for signal and image processing. Dr. Murthy started his career with the Indian Telephone Industries and later with the Electronics and Radar Development Establishment (LRDE) besides spending a few years as a visiting scientist at Concordia University, Canada.

He has a BE from Bangalore University, a ME from the Indian Institute of Science and a PhD from the Technical University of Nova Scotia, Halifax - all in Electrical Engineering. Dr. Rama Murthy is widely published and was the Associate Editor for IEEE Transactions on Circuits and Systems and is the current Associate Editor for Journal of Circuits, Systems and Signal Processing.

Dr. Rama Murthy is a Senior Member of IEEE, Life Member of ACCS and a Fellow of Indian National Academy of Engineering.



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